FIG. 1

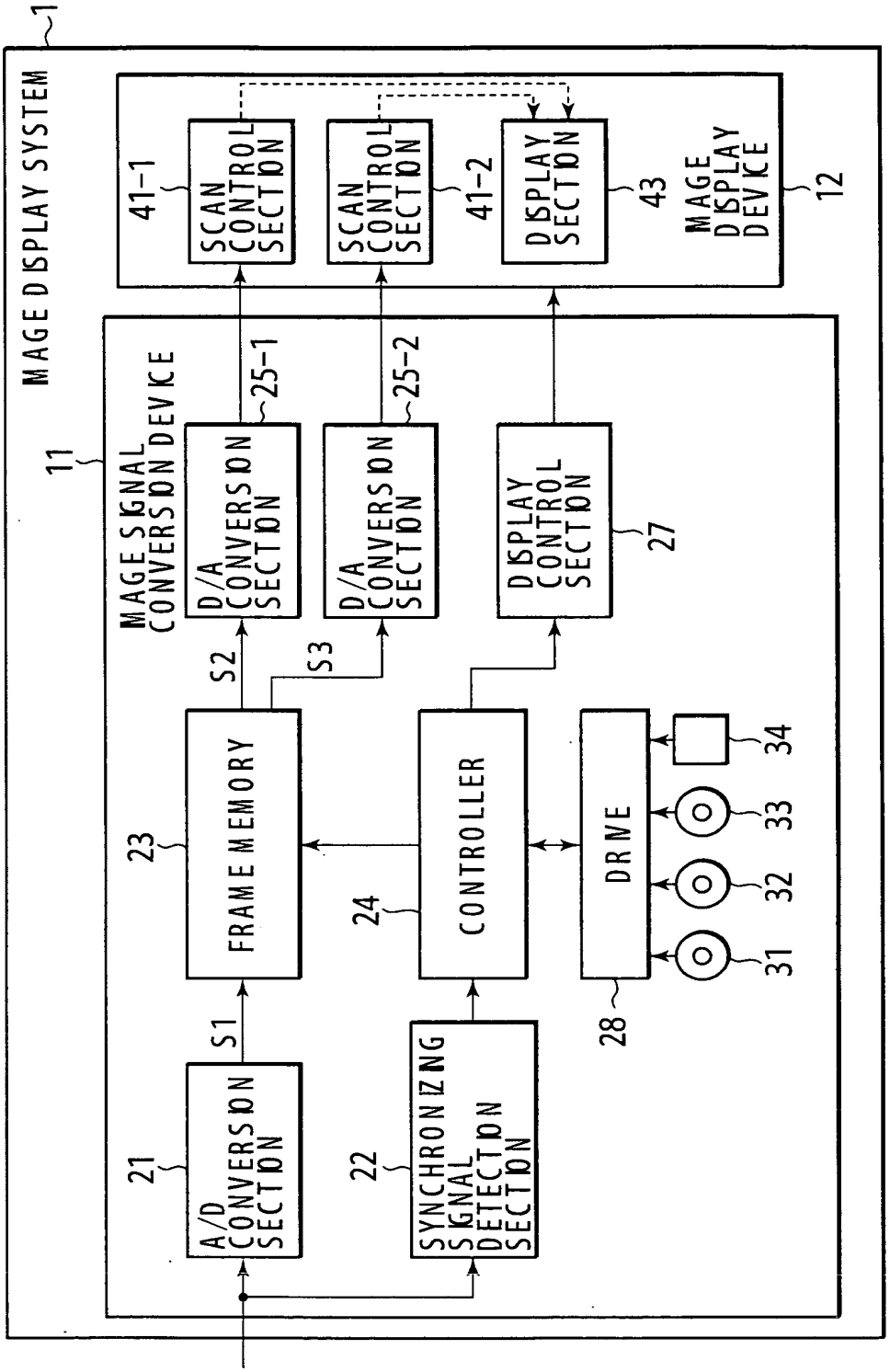

1
MAGE DISPLAY SYSTEM
11
MAGE SIGNAL CONVERSION DEVICE
21
A/D CONVERSION SECTION
22
SYNCHRONIZING SIGNAL DETECTION SECTION
23
FRAME MEMORY
24
CONTROLLER
25-1
D/A CONVERSION SECTION
25-2
D/A CONVERSION SECTION
27
DISPLAY CONTROL SECTION
28
DRIVE
31
32
33
34
S1
S2
S3
12
MAGE DISPLAY DEVICE
41-1
SCAN CONTROL SECTION
41-2
SCAN CONTROL SECTION
43
DISPLAY SECTION

FIG. 2

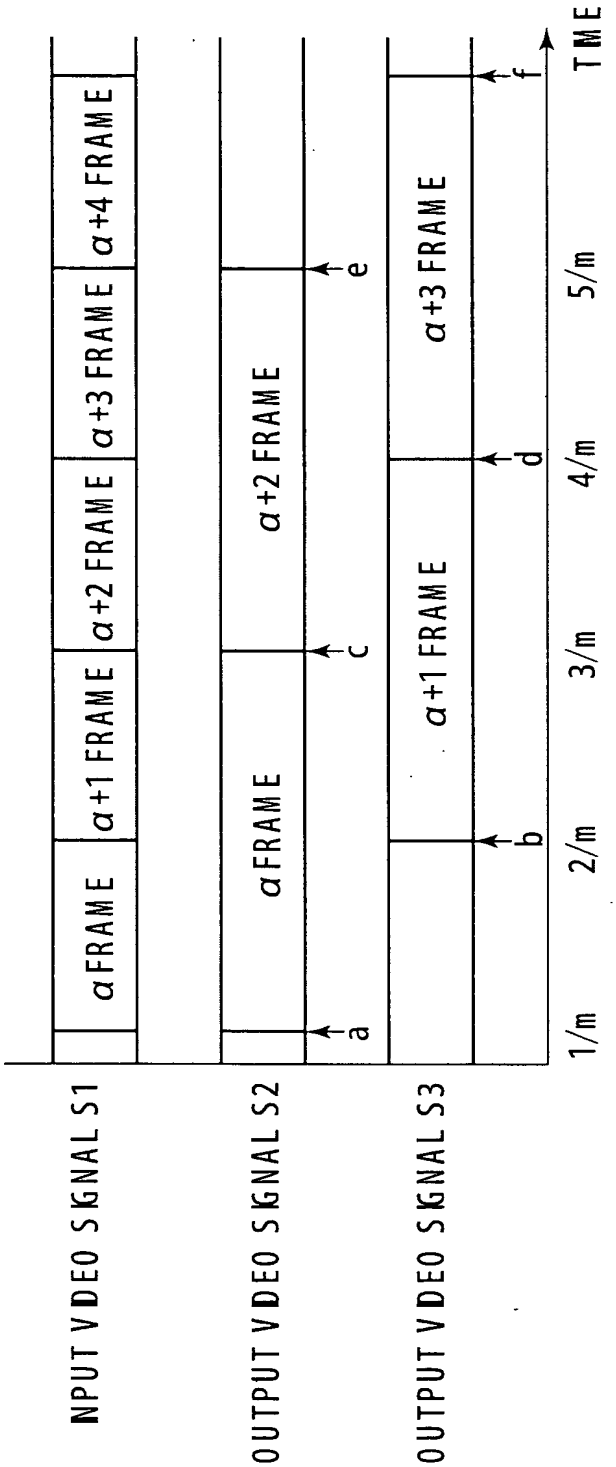

INPUT VIDEO SIGNAL S1
OUTPUT VIDEO SIGNAL S2
OUTPUT VIDEO SIGNAL S3
α FRAME
α+1 FRAME
α+2 FRAME
α+3 FRAME
α+4 FRAME
a
b
c
d
e
f
1/m
2/m
3/m
4/m
5/m
TIME

FIG. 3

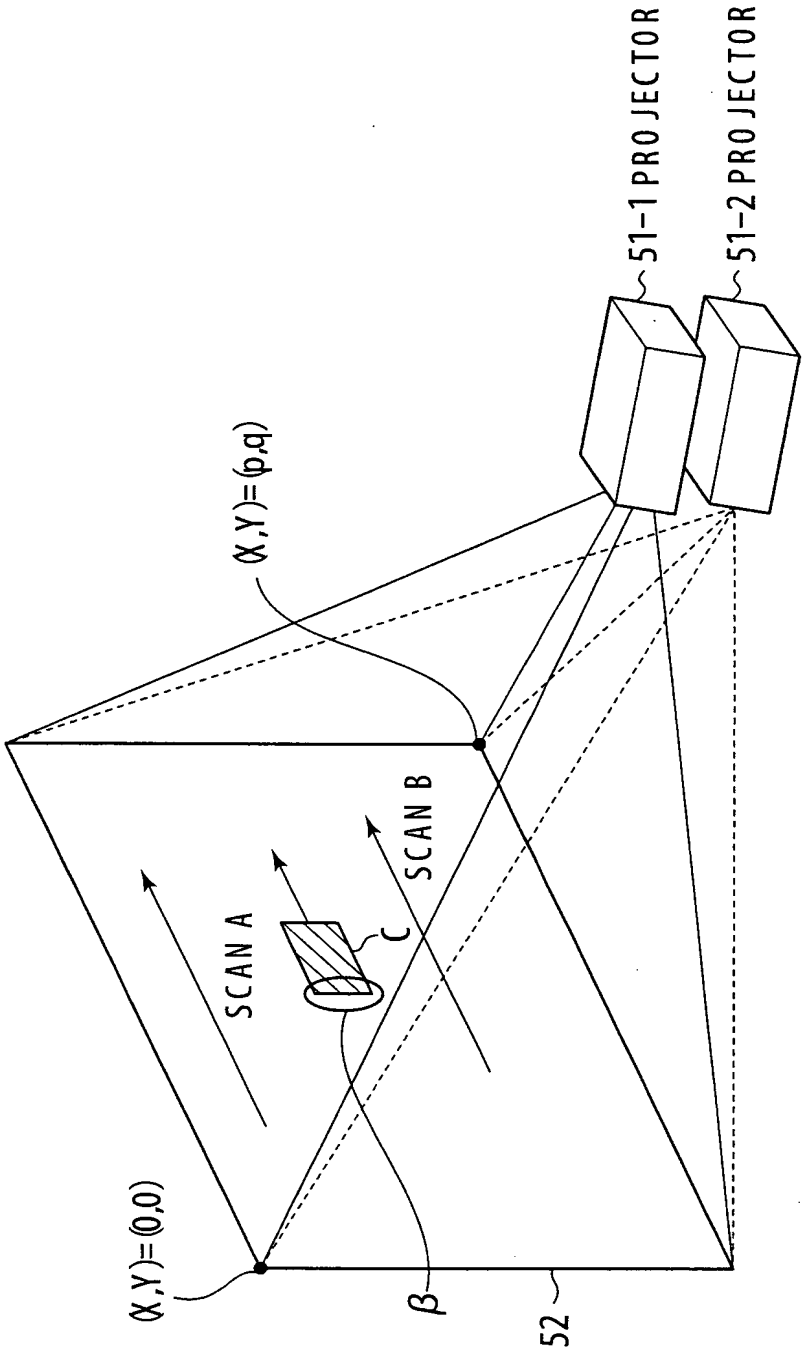


FIG. 4

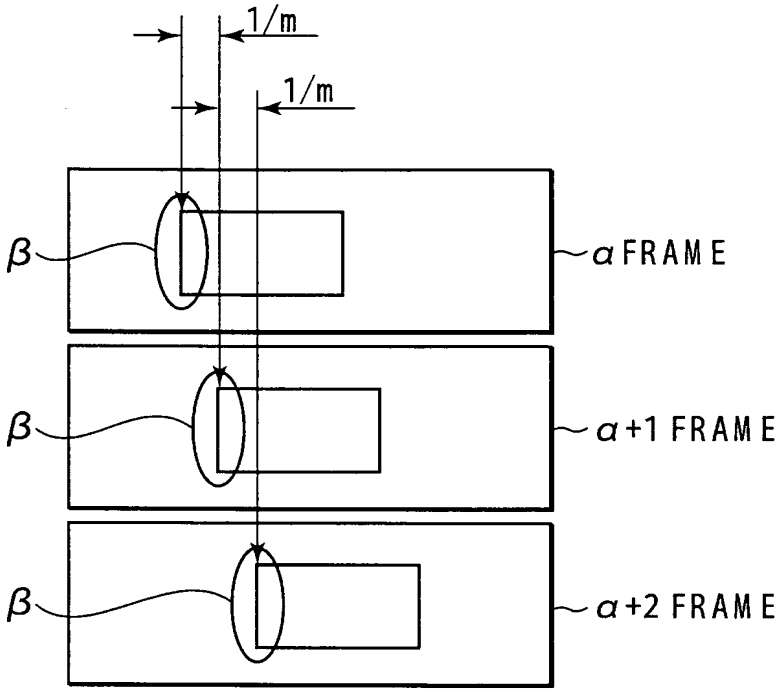


FIG. 5

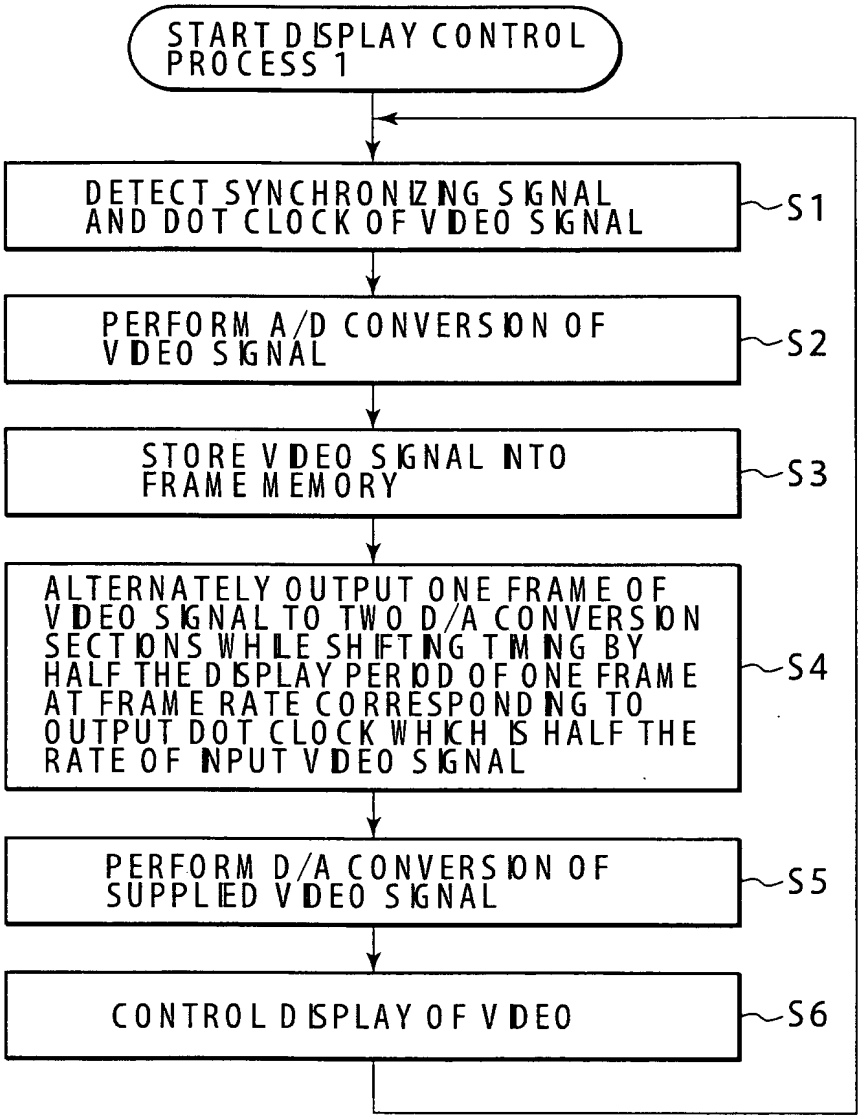

START DISPLAY CONTROL PROCESS 1
DETECT SYNCHRONIZING SIGNAL AND DOT CLOCK OF VIDEO SIGNAL
S1
PERFORM A/D CONVERSION OF VIDEO SIGNAL
S2
STORE VIDEO SIGNAL INTO FRAME MEMORY
S3
ALTERNATELY OUTPUT ONE FRAME OF VIDEO SIGNAL TO TWO D/A CONVERSION SECTIONS WHILE SHIFTING TIMING BY HALF THE DISPLAY PERIOD OF ONE FRAME AT FRAME RATE CORRESPONDING TO OUTPUT DOT CLOCK WHICH IS HALF THE RATE OF INPUT VIDEO SIGNAL
S4
PERFORM D/A CONVERSION OF SUPPLIED VIDEO SIGNAL
S5
CONTROL DISPLAY OF VIDEO
S6

FIG. 6

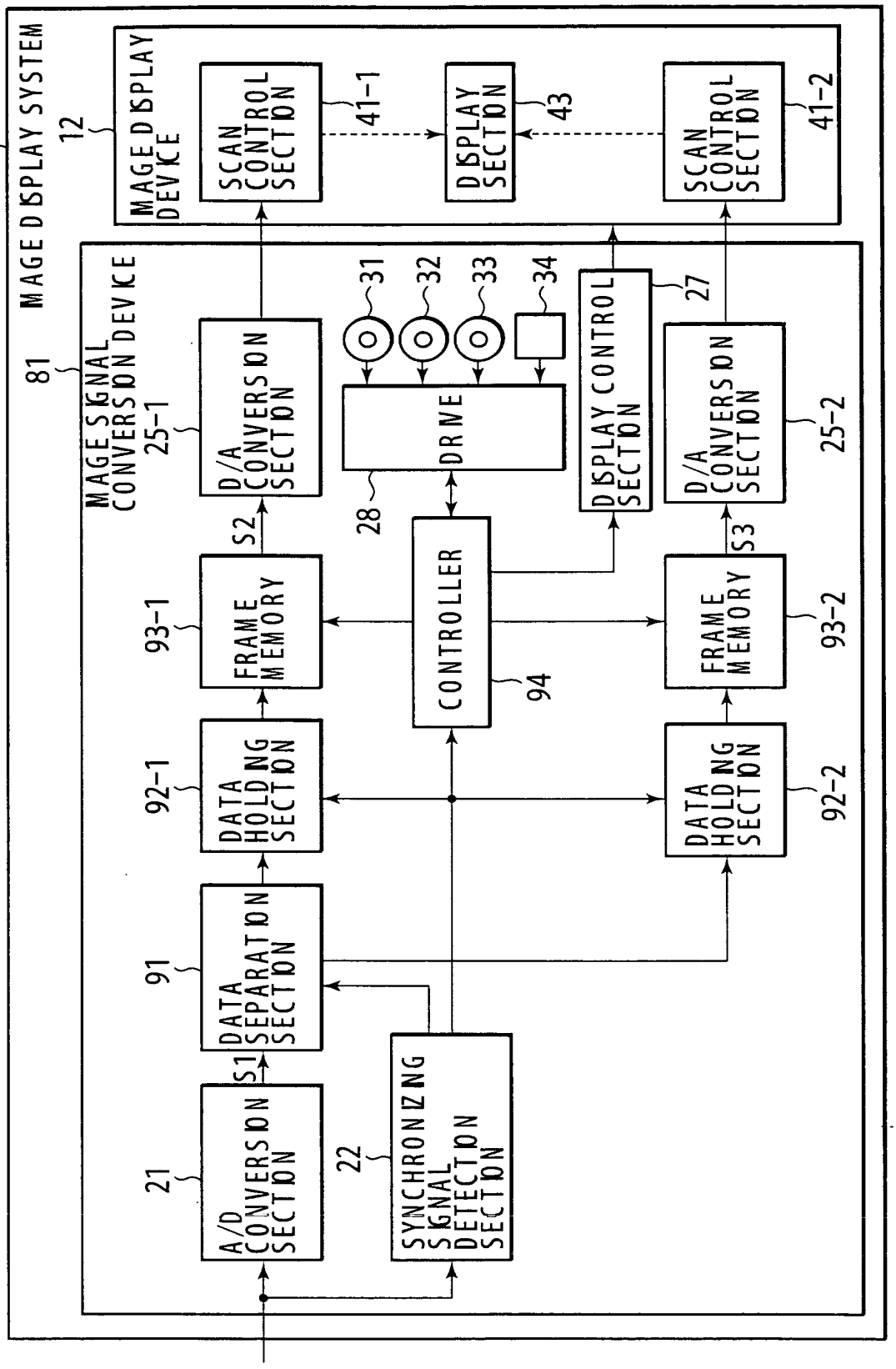

71
IMAGE DISPLAY SYSTEM
12
IMAGE DISPLAY DEVICE
SCAN CONTROL SECTION
41-1
DISPLAY SECTION
43
SCAN CONTROL SECTION
41-2
81
IMAGE SIGNAL CONVERSION DEVICE
25-1
D/A CONVERSION SECTION
31
32
33
34
DRIVE
28
DISPLAY CONTROL SECTION
27
D/A CONVERSION SECTION
25-2
S2
93-1
FRAME MEMORY
CONTROLLER
94
FRAME MEMORY
93-2
S3
92-1
DATA HOLDING SECTION
DATA HOLDING SECTION
92-2
91
DATA SEPARATION SECTION
S1
21
A/D CONVERSION SECTION
22
SYNCHRONIZING SIGNAL DETECTION SECTION

FIG. 7

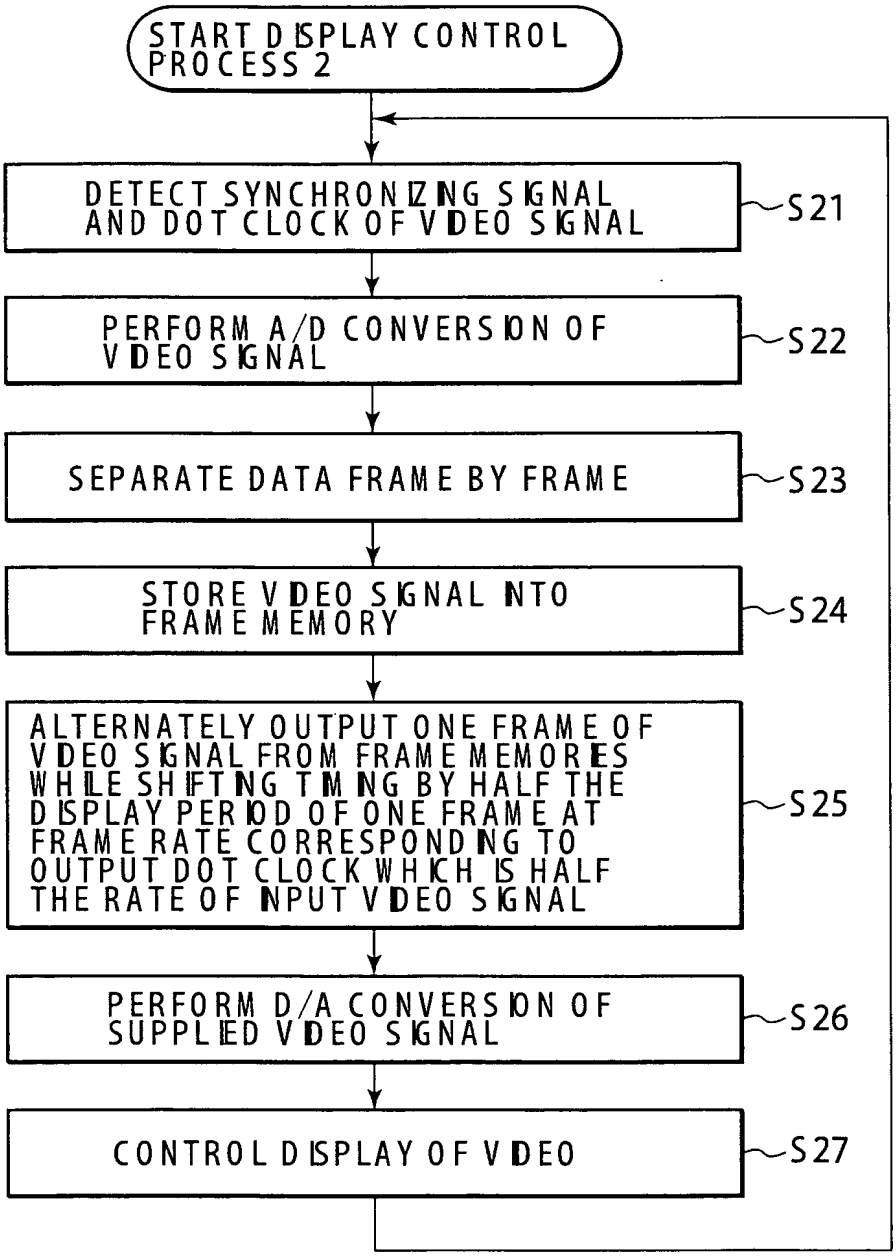

START DISPLAY CONTROL PROCESS 2
DETECT SYNCHRONIZING SIGNAL AND DOT CLOCK OF VIDEO SIGNAL
S21
PERFORM A/D CONVERSION OF VIDEO SIGNAL
S22
SEPARATE DATA FRAME BY FRAME
S23
STORE VIDEO SIGNAL INTO FRAME MEMORY
S24
ALTERNATELY OUTPUT ONE FRAME OF VIDEO SIGNAL FROM FRAME MEMORIES WHILE SHIFTING TIMING BY HALF THE DISPLAY PERIOD OF ONE FRAME AT FRAME RATE CORRESPONDING TO OUTPUT DOT CLOCK WHICH IS HALF THE RATE OF INPUT VIDEO SIGNAL
S25
PERFORM D/A CONVERSION OF SUPPLIED VIDEO SIGNAL
S26
CONTROL DISPLAY OF VIDEO
S27

FIG. 8

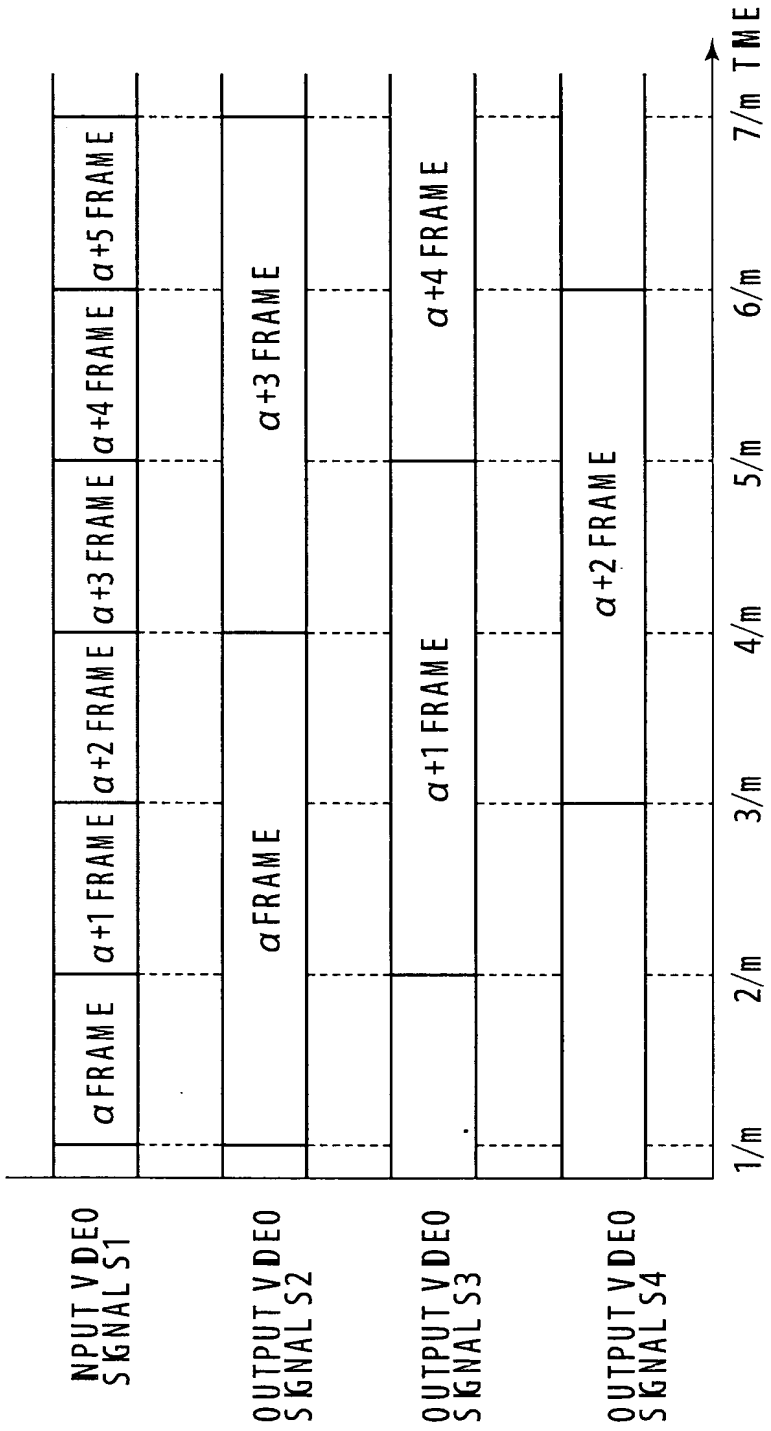

INPUT VIDEO SIGNAL S1
OUTPUT VIDEO SIGNAL S2
OUTPUT VIDEO SIGNAL S3
OUTPUT VIDEO SIGNAL S4
αFRAME
α+1 FRAME
α+2 FRAME
α+3 FRAME
α+4 FRAME
α+5 FRAME
1/m
2/m
3/m
4/m
5/m
6/m
7/m
TIME

FIG. 9

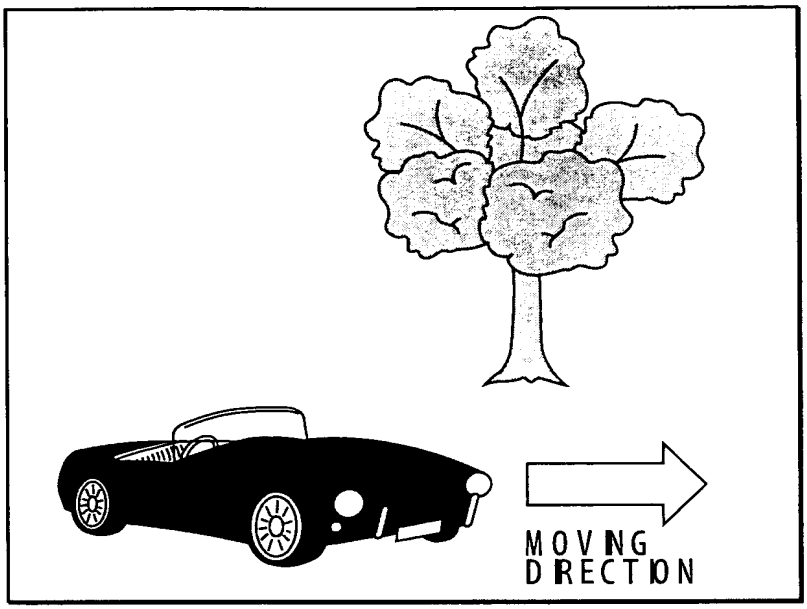


FIG. 10

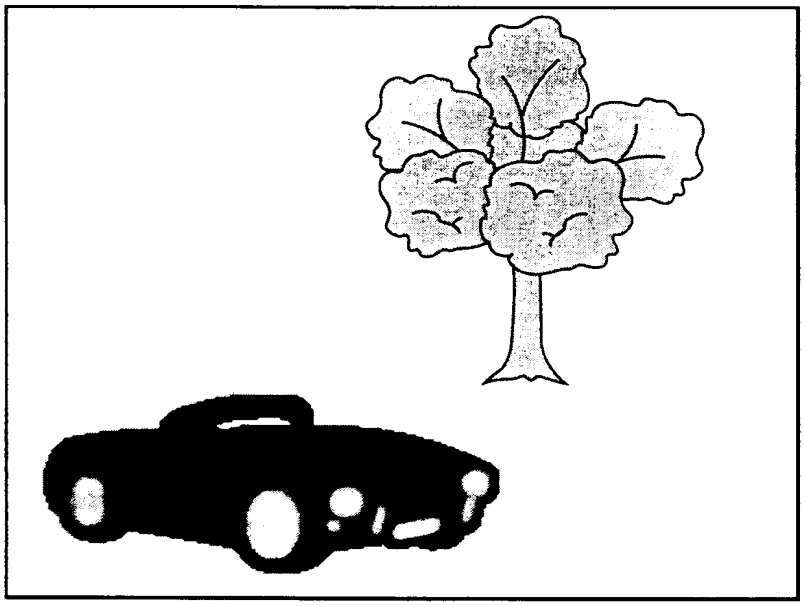


FIG. 11

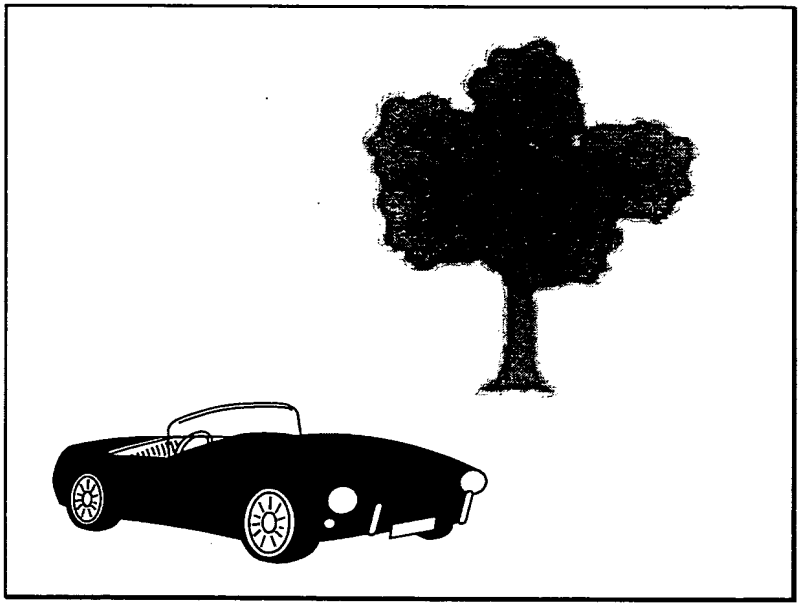
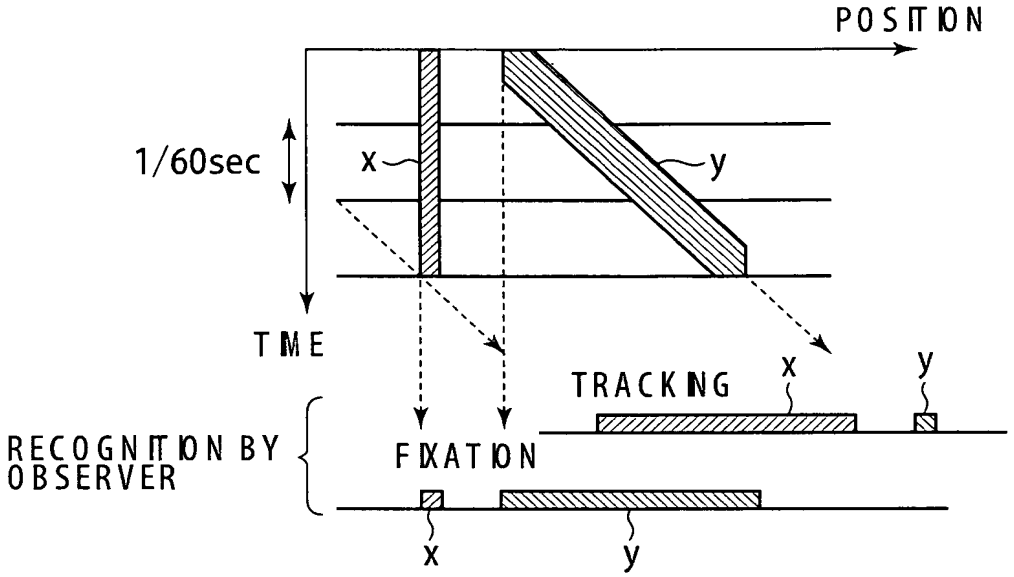


FIG. 12


POSITION
1/60sec
x
y
TIME
TRACKING
x
y
RECOGNITION BY OBSERVER
FIXATION
x
y

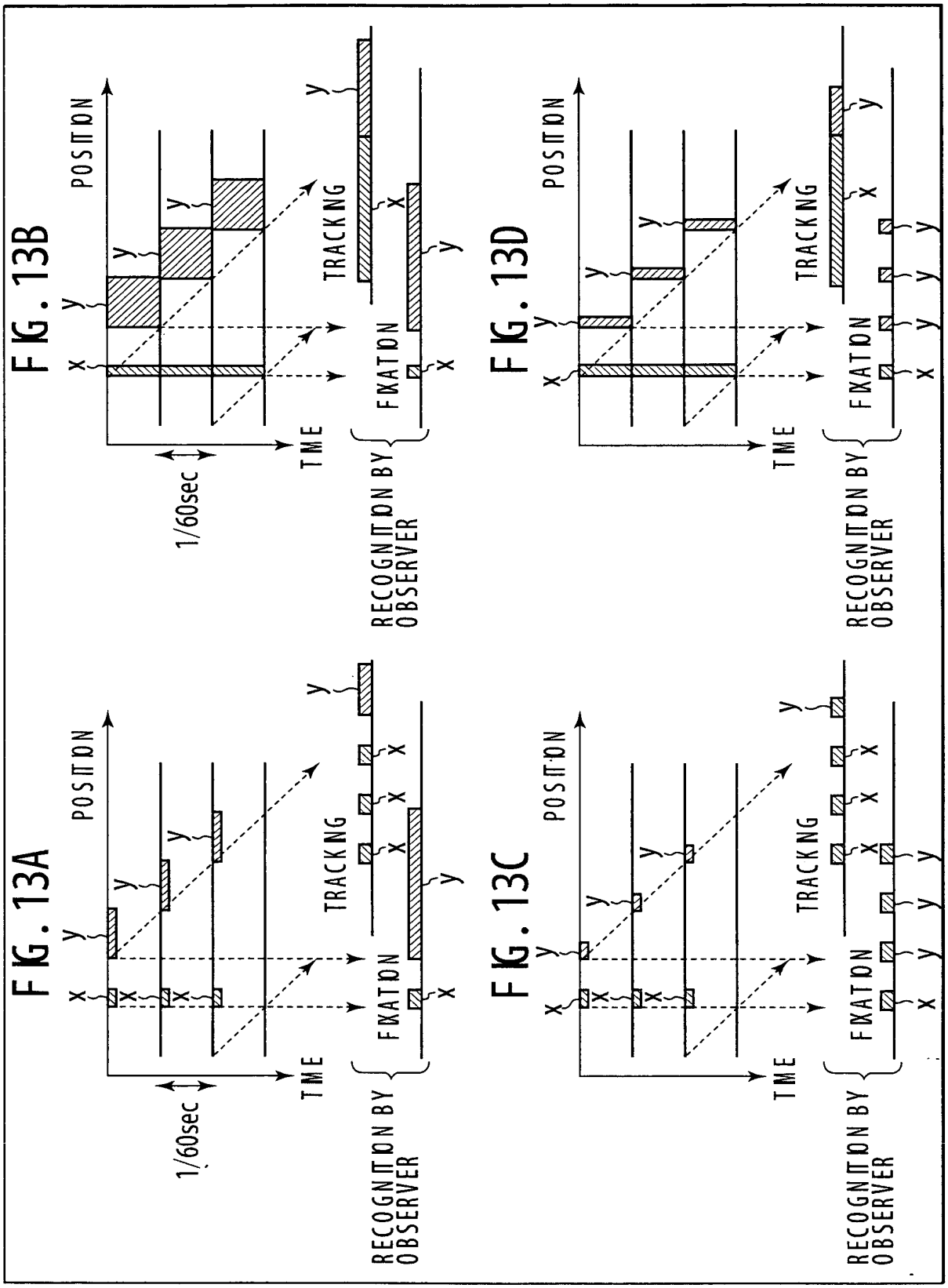
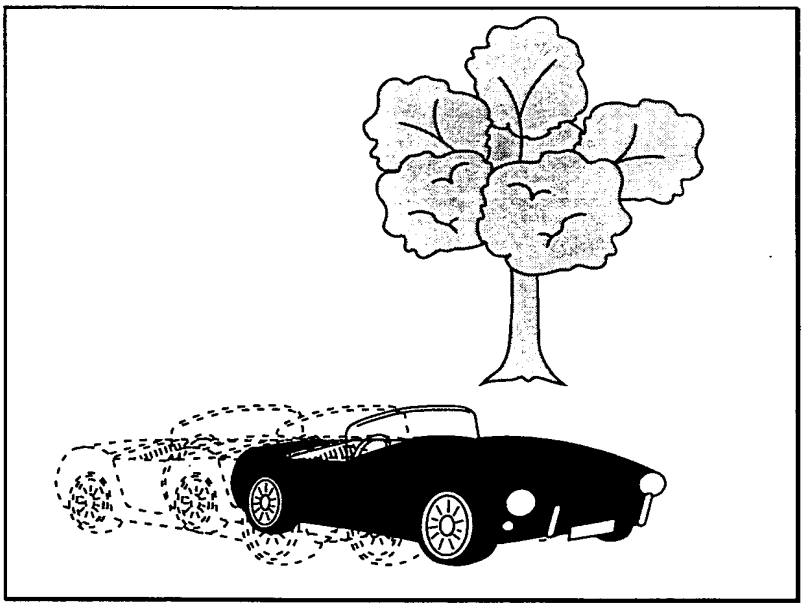


FIG. 13A
POSITION
TRACKING
FIXATION
TIME
1/60sec
RECOGNITION BY OBSERVER
FIG. 13B
POSITION
TRACKING
FIXATION
TIME
1/60sec
RECOGNITION BY OBSERVER
FIG. 13C
POSITION
TRACKING
FIXATION
TIME
RECOGNITION BY OBSERVER
FIG. 13D
POSITION
TRACKING
FIXATION
TIME
RECOGNITION BY OBSERVER

FIG. 14



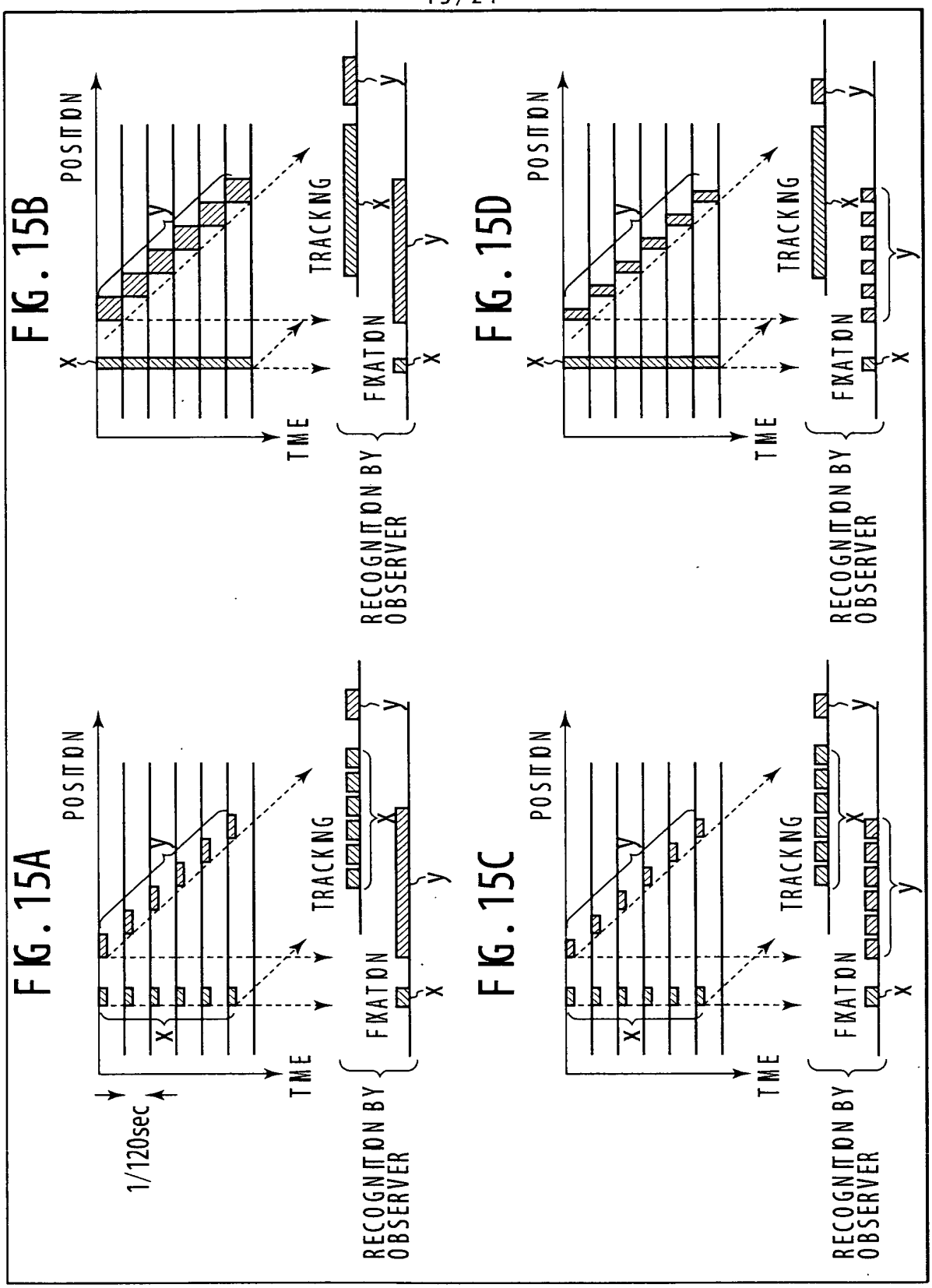


FIG. 15A
POSITION
TRACKING
TIME
1/120sec
FIXATION
RECOGNITION BY OBSERVER
FIG. 15B
POSITION
TRACKING
TIME
FIXATION
RECOGNITION BY OBSERVER
FIG. 15C
POSITION
TRACKING
TIME
FIXATION
RECOGNITION BY OBSERVER
FIG. 15D
POSITION
TRACKING
TIME
FIXATION
RECOGNITION BY OBSERVER

FIG. 16

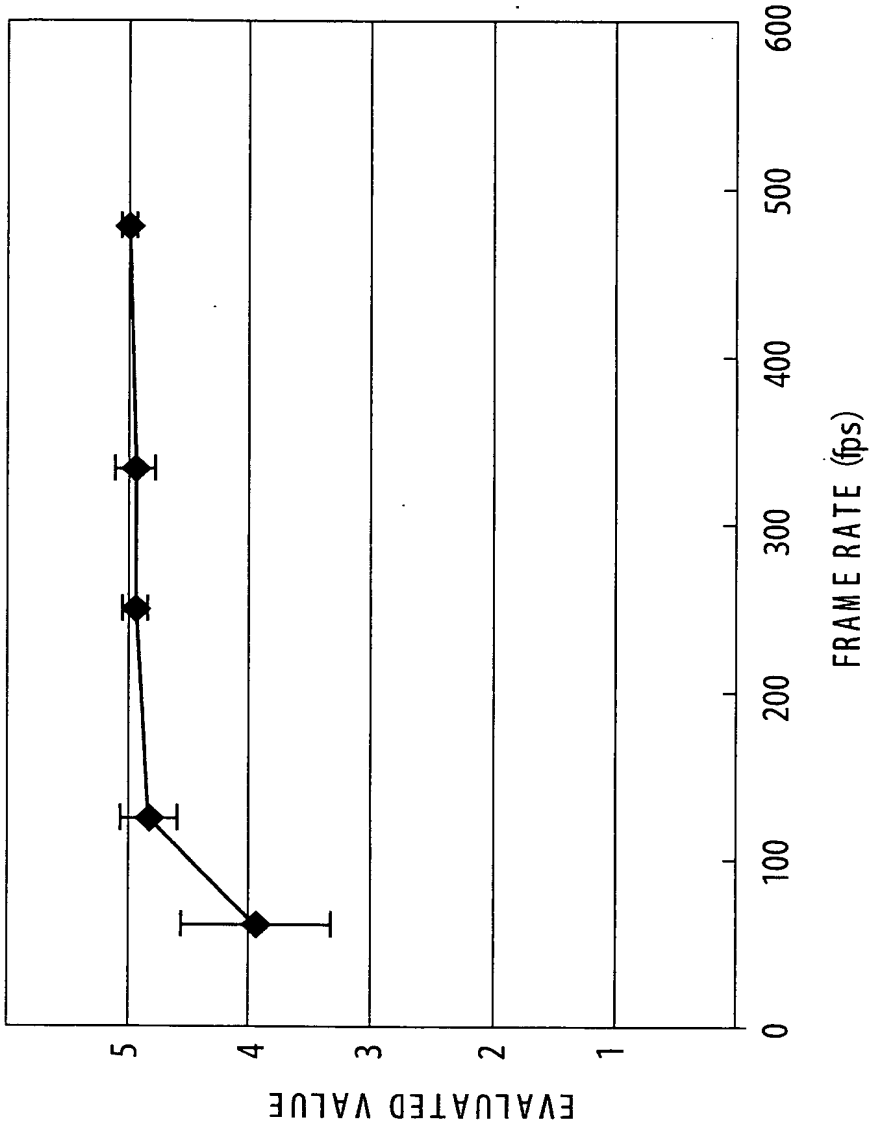

EVALUATED VALUE
5
4
3
2
1
0
100
200
300
400
500
600
FRAME RATE (fps)

FIG. 17

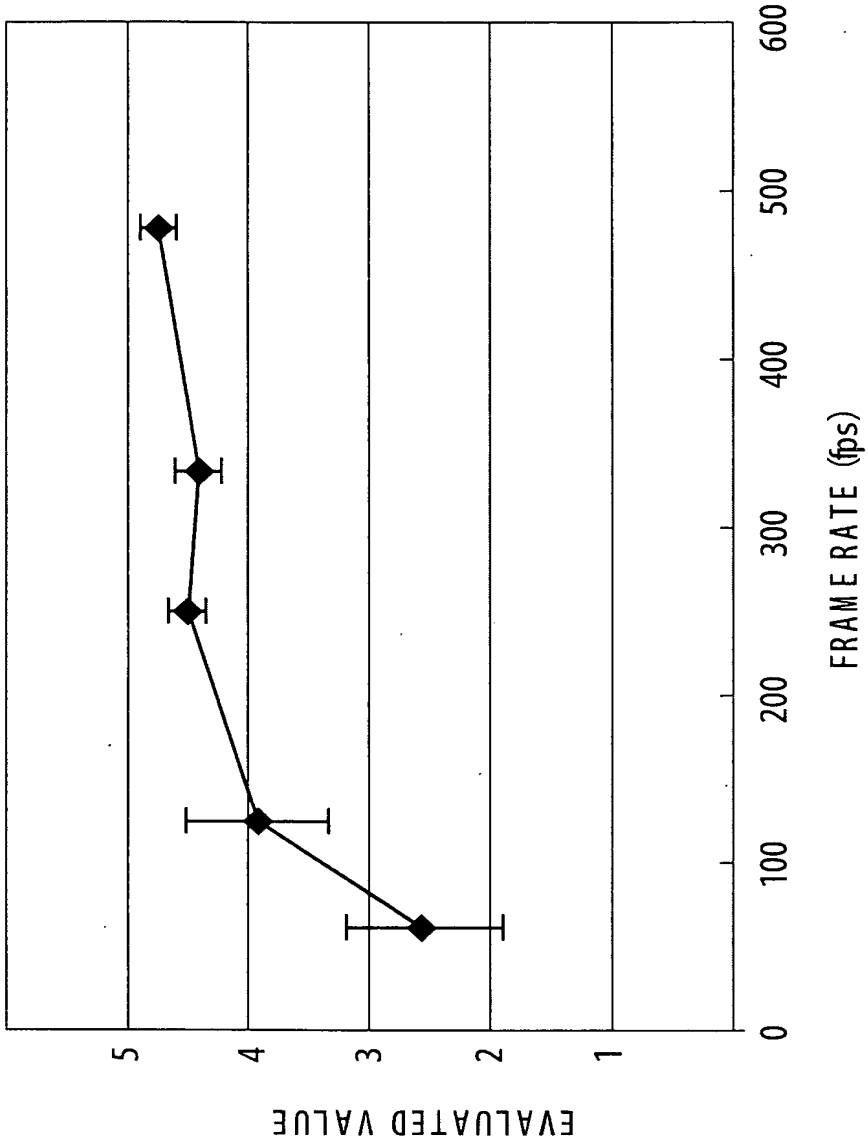

EVALUATED VALUE
5
4
3
2
1
0
100
200
300
400
500
600
FRAME RATE (fps)

FIG. 18

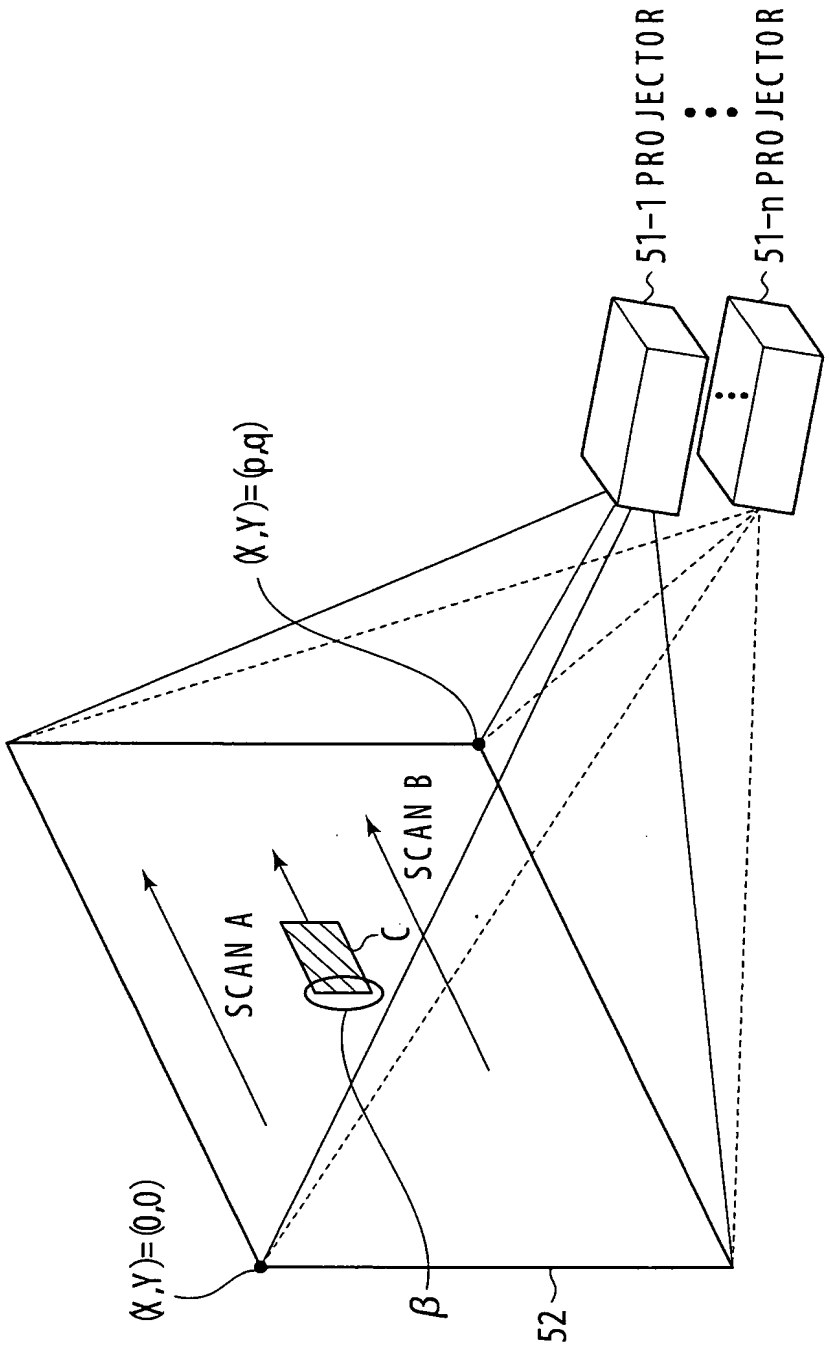


FIG. 19

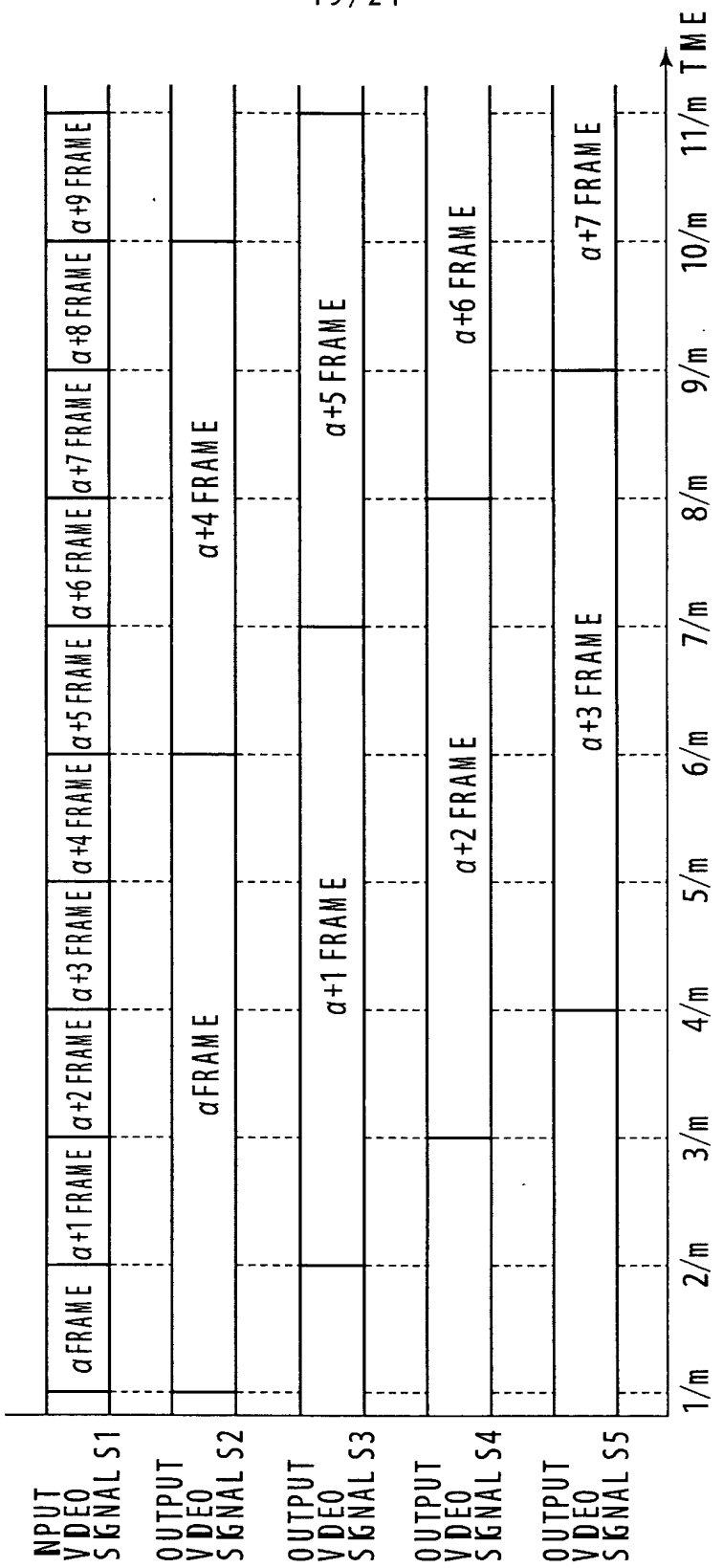

INPUT VIDEO SIGNAL S1
OUTPUT VIDEO SIGNAL S2
OUTPUT VIDEO SIGNAL S3
OUTPUT VIDEO SIGNAL S4
OUTPUT VIDEO SIGNAL S5
αFRAME
α+1 FRAME
α+2 FRAME
α+3 FRAME
α+4 FRAME
α+5 FRAME
α+6 FRAME
α+7 FRAME
α+8 FRAME
α+9 FRAME
1/m
2/m
3/m
4/m
5/m
6/m
7/m
8/m
9/m
10/m
11/m
TIME

FIG. 20

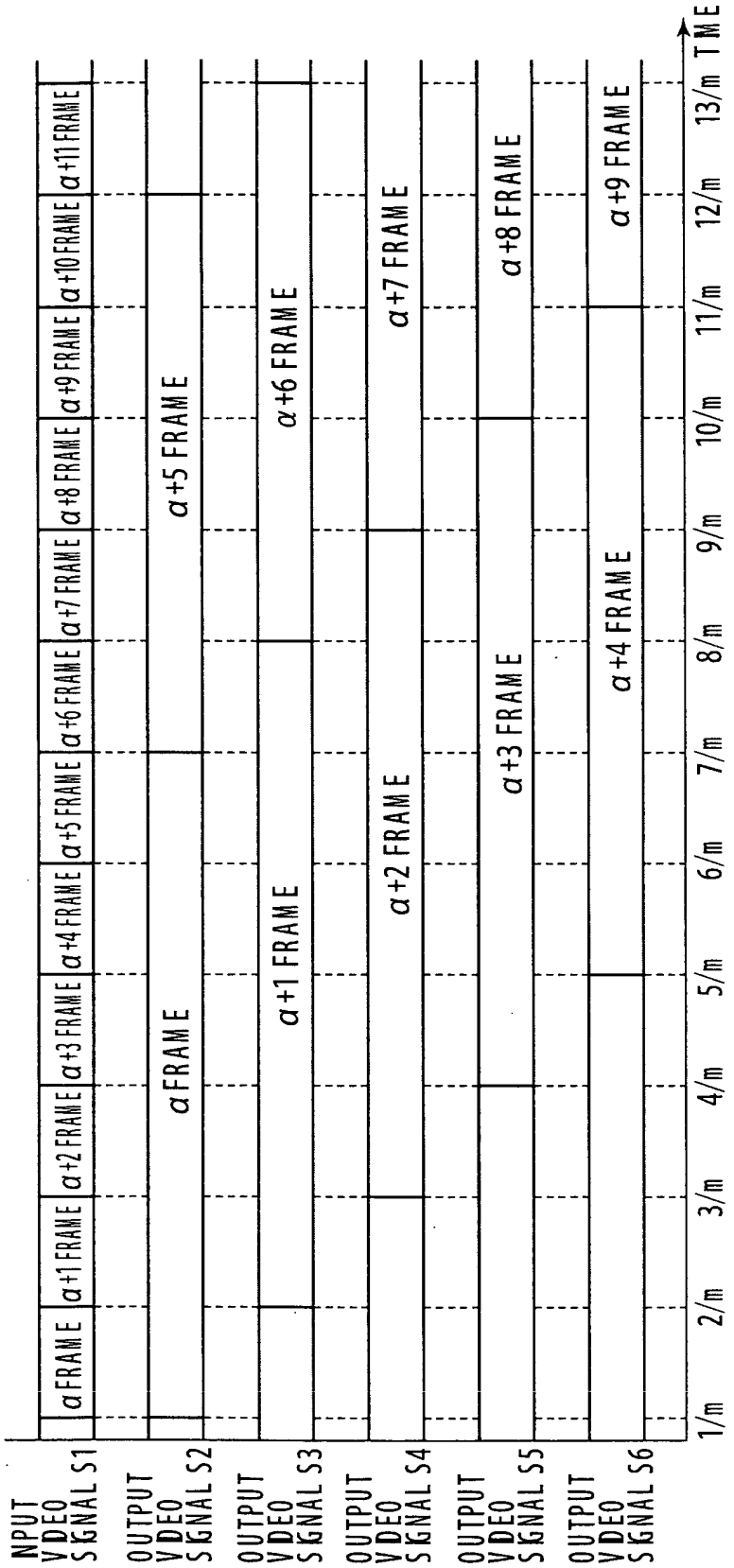
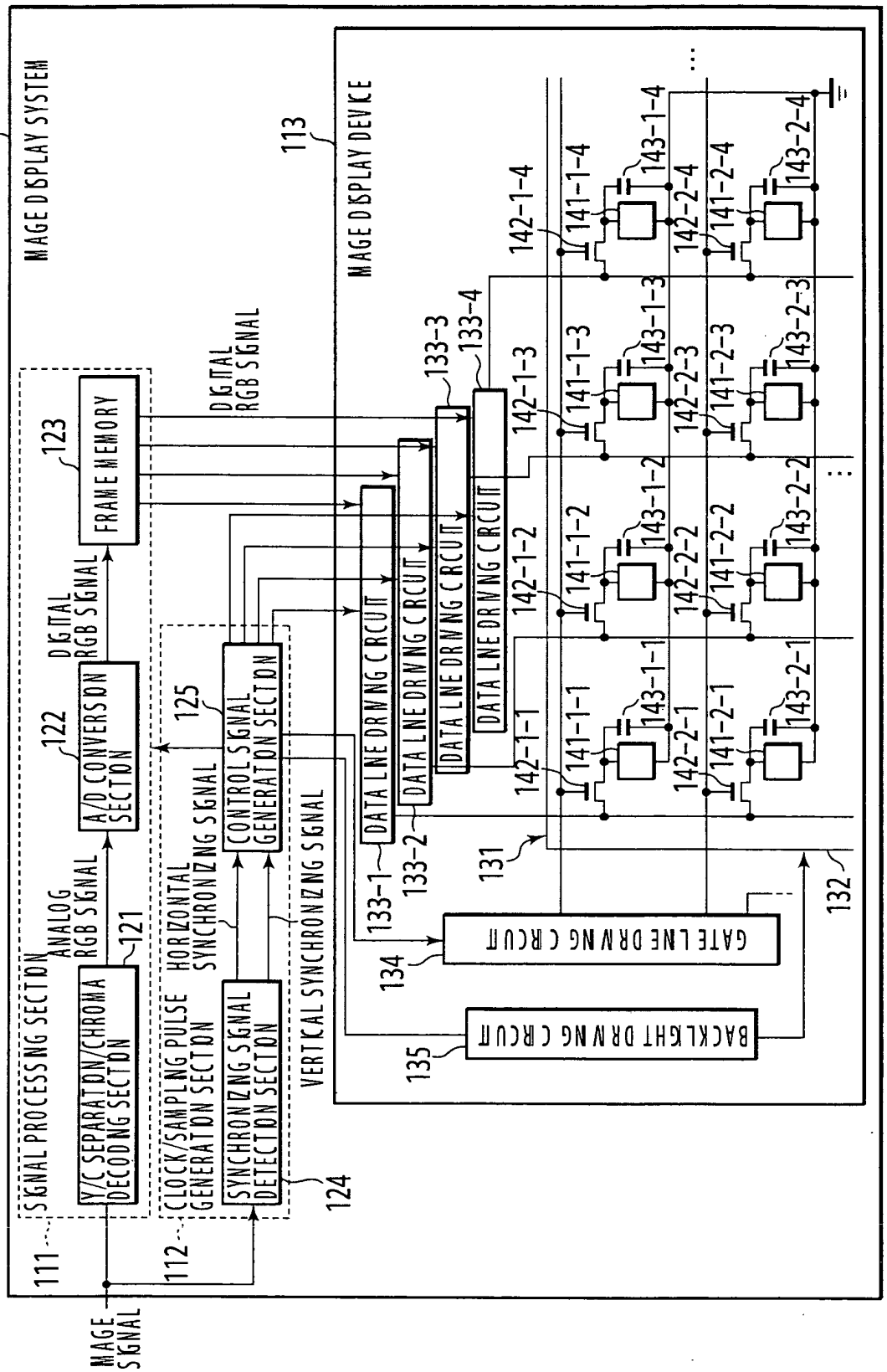

INPUT VIDEO SIGNAL S1
OUTPUT VIDEO SIGNAL S2
OUTPUT VIDEO SIGNAL S3
OUTPUT VIDEO SIGNAL S4
OUTPUT VIDEO SIGNAL S5
OUTPUT VIDEO SIGNAL S6
αFRAME
α+1 FRAME
α+2 FRAME
α+3 FRAME
α+4 FRAME
α+5 FRAME
α+6 FRAME
α+7 FRAME
α+8 FRAME
α+9 FRAME
α+10 FRAME
α+11 FRAME
1/m
2/m
3/m
4/m
5/m
6/m
7/m
8/m
9/m
10/m
11/m
12/m
13/m
TIME

FIG. 21


101
MAGE DISPLAY SYSTEM
113
MAGE DISPLAY DEVICE
SIGNAL PROCESSING SECTION
111
Y/C SEPARATION/CHROMA DECODING SECTION
121
ANALOG RGB SIGNAL
A/D CONVERSION SECTION
122
DIGITAL RGB SIGNAL
FRAME MEMORY
123
DIGITAL RGB SIGNAL
CLOCK/SAMPLING PULSE GENERATION SECTION
112
SYNCHRONIZING SIGNAL DETECTION SECTION
124
HORIZONTAL SYNCHRONIZING SIGNAL
VERTICAL SYNCHRONIZING SIGNAL
CONTROL SIGNAL GENERATION SECTION
125
MAGE SIGNAL
DATA LINE DRIVING CIRCUIT
133-1
133-2
133-3
133-4
GATE LINE DRIVING CIRCUIT
134
BACKLIGHT DRIVING CIRCUIT
135
131
132
142-1-1
142-1-2
142-1-3
142-1-4
141-1-1
141-1-2
141-1-3
141-1-4
143-1-1
143-1-2
143-1-3
143-1-4
142-2-1
142-2-2
142-2-3
142-2-4
141-2-1
141-2-2
141-2-3
141-2-4
143-2-1
143-2-2
143-2-3
143-2-4